PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION	NO.
200209635-1			
APPLICANT			
Norbert R. SEIFERT et	al.		
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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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1L					
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

/AGS/	10	R. Ramanarayanan et al., "Analysis of Soft Error Rate in Flip-Flops and Scannable Latches," Microsystems Design Laboratory, 4 pp.	
/AGS/	1R	K. Mohanram et al. "Cost-Effective Approach for Reducing Soft Error Failure Rate in Logic Circuits," University of Texas at Austin, 9 pp.	
AND DESCRIPTION OF THE PERSON	***************************************		
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EXAMIN	ER	DATE CONSIDERED	
/Ale	/Alexander Sofocleous/ 06/27/2008		

Rev 10/03 (PTO1449)